

AMENDMENTS TO THE CLAIMS

The following listing of claims will replace all prior versions and listings of claims in the application.

LISTING OF CLAIMS

1. (Currently amended) A method for dynamic allocation of slot bandwidth on a switch, comprising:

~~setting providing~~ $B/\Delta B$ pieces of N-selected-one devices, ~~and~~ input bandwidth of every N-selected-one device being $N*\Delta B$; ~~wherein, N denoting~~ the number of slots for dynamic bandwidth allocation, B denoting bandwidth need to be dynamically allocated; ~~and ΔB denoting~~ a minimum allocated bandwidth unit;

~~connecting~~ communicating each slot with ~~one~~ an input of each N-selected-one device, and communicating ~~connecting~~ all outputs of the N-selected-one devices with a main switch module;

controlling the N-selected-one devices ~~being gated~~ to allocate the bandwidth to ~~gated~~ communicated slots.

2. (Currently amended) The method according to Claim 1, further comprising:

controlling, by the main switch module, a programmable logic chip to output strobe signals; ~~and~~

wherein the controlling the N-selected-one devices ~~being gated~~ to allocate the bandwidth to ~~gated~~ communicated slots comprises: controlling the N-selected-one devices ~~being gated~~ by the programmable logic chip through the strobe signals.

3. (Previously presented) The method according to Claim 1, wherein the programmable logic chip is an Electrically Programmable Logical Device (EPLD) with type EPM7256AEC208-10.

4. (Original) The method according to Claim 1, wherein the N-selected-one device is a two-selected-one device.

5. (Original) The method according to Claim 4, wherein the two-selected-one device is a 1.25GHz Ethernet signal driver with type VSC7132YB.

6. (Currently amended) An apparatus for dynamic allocation of slot bandwidth, comprising:

N slots, ~~wherein N denoting~~es the number of slots for dynamic bandwidth allocation;

$B/\Delta B$ pieces of N-selected-one devices, input bandwidth of every N-selected-one device being $N \cdot \Delta B$; ~~wherein B denoting~~es bandwidth need to be dynamically allocated; ~~and ΔB denoting~~es a minimum allocated bandwidth unit; ~~and N inputs of each N-selected-one device are connected with the N slots respectively, and an output of each N-selected-one device is connected with a main switch module;~~

~~the~~ a main switch module;

wherein N inputs of each N-selected-one device communicate with the N slots respectively, an output of each N-selected-one device communicates with the main switch module, and the main switch module communicates with the N-selected-one

~~devices arranged to for controlling~~ the N-selected-one devices ~~being gated~~ to allocate the bandwidth to ~~gated communicated slots~~.

7. (Currently amended) The apparatus according to claim 6, further comprising:

a programmable logic chip ~~controlled by the main switch module, arranged to for~~ output ~~providing~~ strobe signals to control the N-selected-one devices, ~~being gated under control of the main switch module.~~

8. (Previously presented) The apparatus according to claim 7, wherein the programmable logic chip is an Electrically Programmable Logical Device (EPLD).

9. (Currently amended) An apparatus for dynamic allocation of slot bandwidth, comprising:

two slots;

$B/\Delta B$ pieces of two-selected-one devices, input bandwidth of every two-selected-one device being $2*\Delta B$; ~~wherein B denotinges~~ bandwidth need to be dynamically allocated, ~~and~~ ΔB denotinges a minimum allocated bandwidth unit; ~~and two inputs of each two-selected-one device are connected with the two slots respectively, and an output of each two-selected-one device is connected with a main switch module;~~

the main switch module,

wherein two inputs of each two-selected-one device communicate with the two slots respectively, an output of each two-selected-one device communicates with the main switch module, and the main switch module communicates with the two-selected-

~~one devices arranged to fro controlling~~ the two-selected-one devices being gated to
allocate the bandwidth to ~~gated~~ communicated slots.